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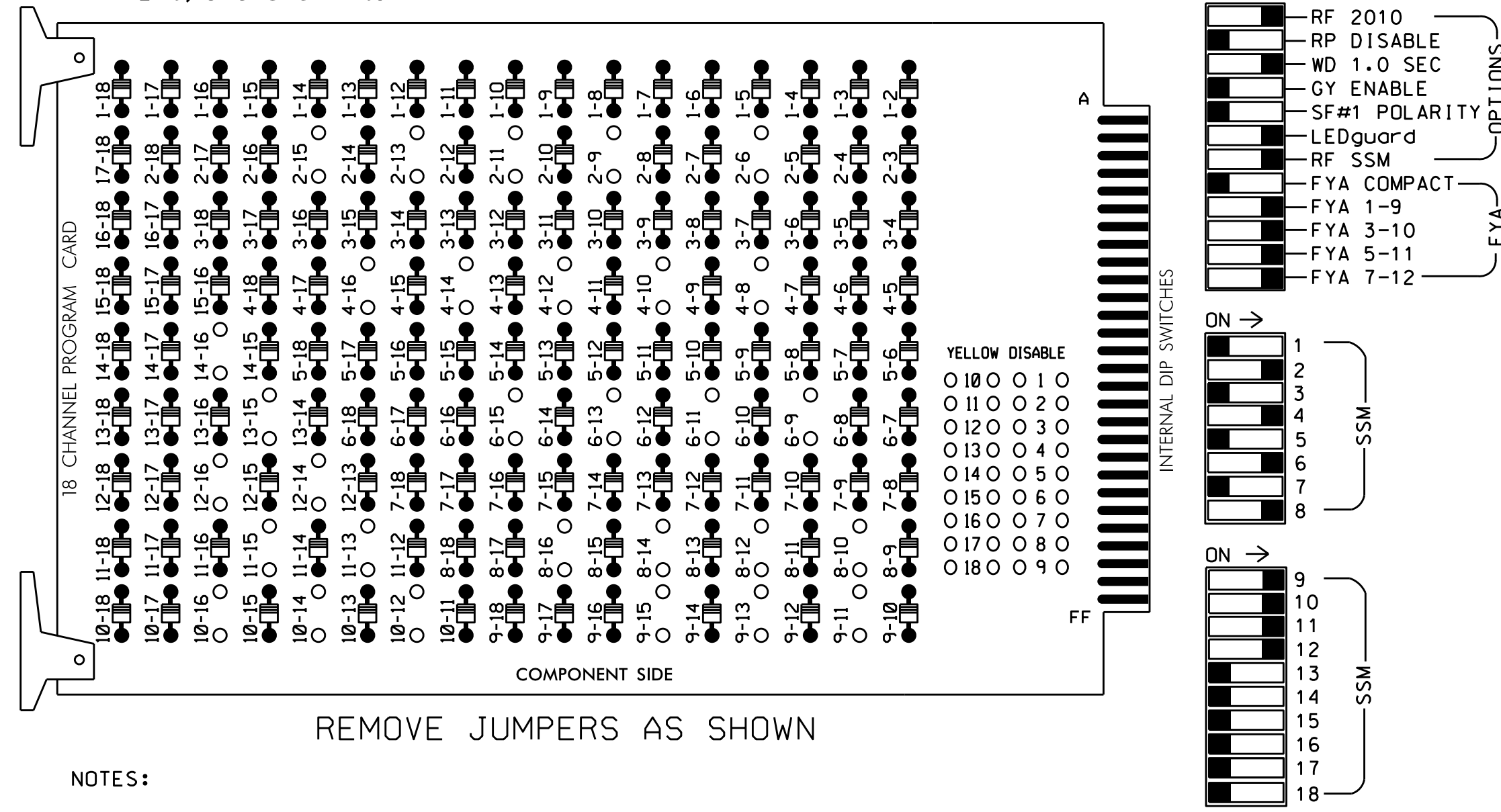
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18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-10, 4-12, 4-14, 4-16, 6-9, 6-11, 6-13, 6-15, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in phases 2 and 6 Green/Don't Walk.
- Enable simultaneous gap-out feature for all phases.
- Program phases 2 and 6 for volume density operation.
- Program phases 4 and 8 for dual entry.
- The cabinet and controller are part of the Raleigh Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/ AUX
 SOFTWARE.....SE-PAC2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S3,S5,S6,S8,S9,S11,S12,AUX S1,
 AUX S2,AUX S4,AUX S5
 PHASES USED.....2,2PED,4,4PED,6,6PED,8,8PED
 OVERLAP "A".....*
 OVERLAP "B".....*
 OVERLAP "C".....*
 OVERLAP "D".....*

* See sheet 2 for Overlap Programming Detail

SIGNAL HEAD HOOK-UP CHART

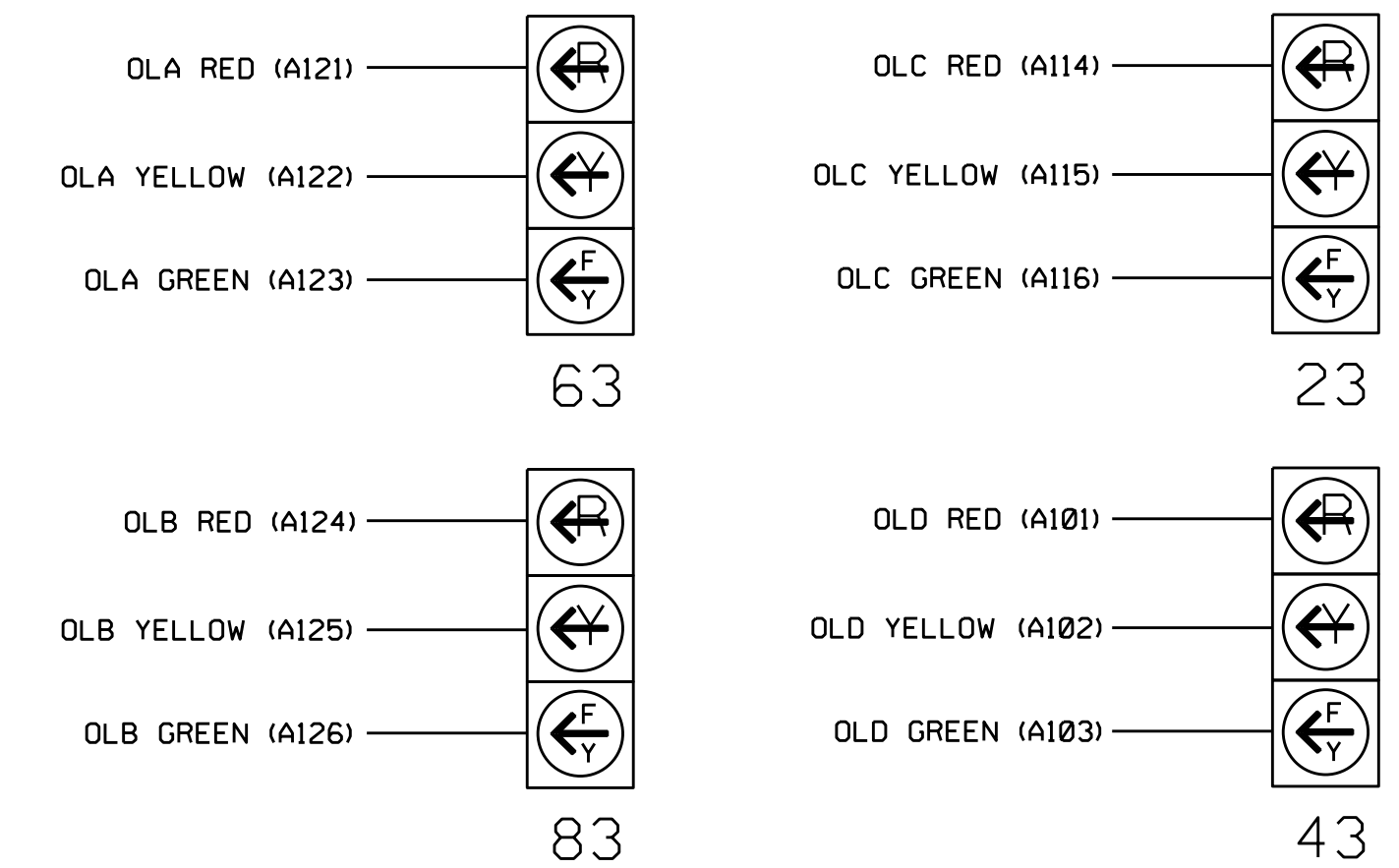
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	P21, P22	NU	41,42	P41, P42	NU	61,62	P61, P62	NU	81,82	P81, P82	63	83	NU	23	43	NU
RED		128		101				134			107							
YELLOW		129		102				135			108							
GREEN		130		103				136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
Hand icon			113		104			119		110								
Walking person icon			115		106			121		112								

NU = Not Used

* See pictorial of head wiring in detail this sheet.

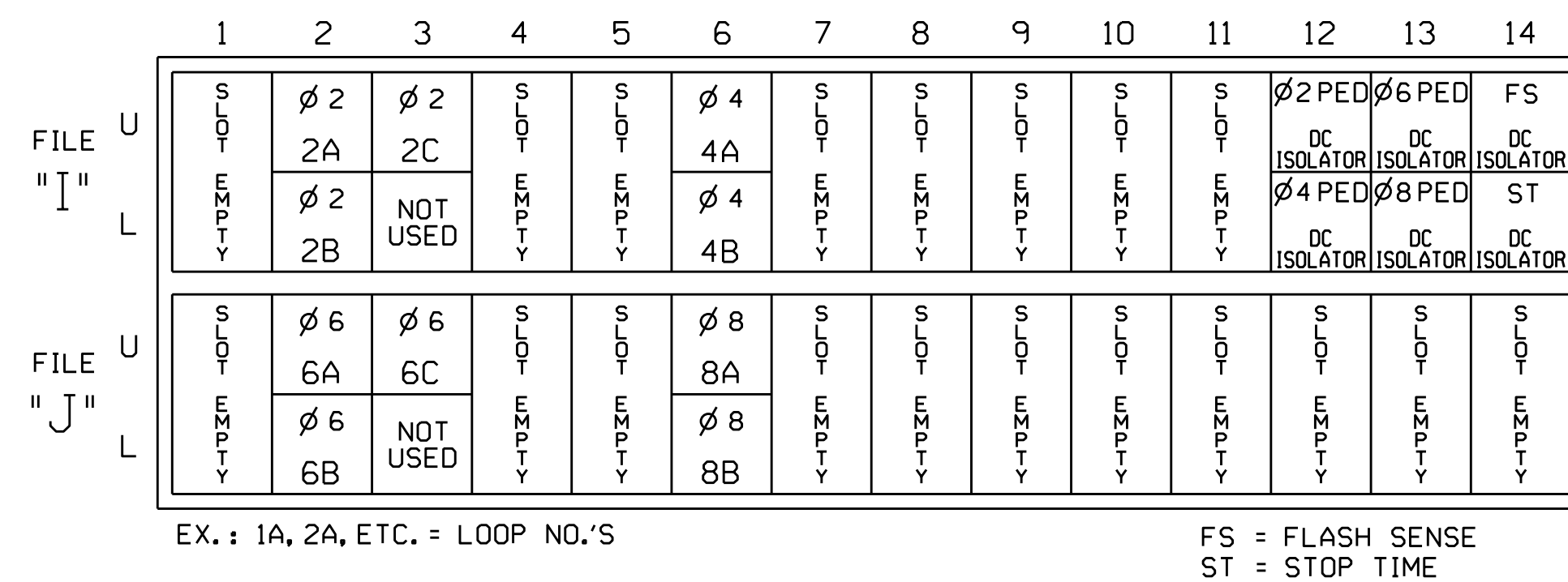
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	DELAY TIME	EXTEND (STRETCH) TIME
2A	TB2-5,6	I2U	39	3	2		
2B	TB2-7,8	I2L	43	4	2		
2C	TB2-9,10	I3U	63	5	2		
4A	TB4-9,10	I6U	41	11	4		
4B	TB4-11,12	I6L	45	12	4	10	
6A	TB3-5,6	J2U	40	21	6		
6B	TB3-7,8	J2L	44	22	6		
6C	TB3-9,10	J3U	64	23	6		
8A	TB5-9,10	J6U	42	31	8		
8B	TB5-11,12	J6L	46	32	8	10	
PED PUSH BUTTONS							
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED		
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED		
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED		
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED		

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

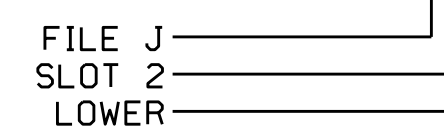
FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

INPUT FILE POSITION LEGEND: J2L



This plan supersedes the plan signed and sealed on 5/24/2021.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-2247
 DESIGNED: February 2024
 SEALED: 02/27/2024
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared in the Offices of:

 750 N. Greenfield Pkwy, Corner, NC 27529

SR 2215 (Buffaloe Road) at Valley Stream Drive

Division 5 Wake County Raleigh

PLAN DATE: February 2024 REVIEWED BY:
 PREPARED BY: S. Kirkpatrick REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by:

 Ryan W. Hough 02/27/2024

430300FA06833
 DATE
 SIG. INVENTORY NO. 05-2247

27-FEB-2024 14:42 S:\IT\5\KTS\Sigonal\Workgroups\519 Man\Projects From Signal Design\Mch1\ve Projects\Kirkpatrick\05-1780 & 05-2247 (HS-2005G)W52247_sm.ele (a.20240227.dgn) sgc:rkp:ck

OVERLAP PROGRAMMING DETAIL

- From Main Menu select **4 - UNIT DATA**
- From UNIT DATA Submenu select **3 - OVERLAP DATA**

```
OVERLAP DATA
A: FYA  E: ---  I: ---  M: ---
B: FYA  F: ---  J: ---  N: ---
C: FYA  G: ---  K: ---  O: ---
D: FYA  H: ---  L: ---  P: ---
PREV/NEXT TO CYCLE
```

Press ESC

OVERLAP A

Use Up/Dn/Left/Right keys to position cursor on Overlap 'A', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - A      DELAY/10:  0
PHASES..12345678  90123456
PERM PHASES: 01000000 00000000
PROT PHASES: 10000000 00000000
-PED PHASES: 00000000 00000000
OVERLAPS..ABCDEFGH IJKLMNQP
PERM OVERLAPS: x0000000 00000000
PROT OVERLAPS: x0000000 00000000
```

NOTICE DELAY/10 = 0

Press ESC

OVERLAP B

Use Up/Dn/Left/Right keys to position cursor on Overlap 'B', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - B      DELAY/10:  0
PHASES..12345678  90123456
PERM PHASES: 00010000 00000000
PROT PHASES: 00100000 00000000
-PED PHASES: 00000000 00000000
OVERLAPS..ABCDEFGH IJKLMNQP
PERM OVERLAPS: 0x000000 00000000
PROT OVERLAPS: 0x000000 00000000
```

NOTICE DELAY/10 = 0

Press ESC

OVERLAP C

Use Up/Dn/Left/Right keys to position cursor on Overlap 'C', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - C      DELAY/10:  0
PHASES..12345678  90123456
PERM PHASES: 00000100 00000000
PROT PHASES: 00001000 00000000
-PED PHASES: 00000000 00000000
OVERLAPS..ABCDEFGH IJKLMNQP
PERM OVERLAPS: 00x00000 00000000
PROT OVERLAPS: 00x00000 00000000
```

NOTICE DELAY/10 = 0

Press ESC

OVERLAP D

Use Up/Dn/Left/Right keys to position cursor on Overlap 'D', use the NEXT key to select 'FYA', then press ENT

```
FYA OVERLAP - D      DELAY/10:  0
PHASES..12345678  90123456
PERM PHASES: 00000001 00000000
PROT PHASES: 00000010 00000000
-PED PHASES: 00000000 00000000
OVERLAPS..ABCDEFGH IJKLMNQP
PERM OVERLAPS: 000x0000 00000000
PROT OVERLAPS: 000x0000 00000000
```

NOTICE DELAY/10 = 0

END OVERLAP PROGRAMMING

ADVANCE WALK PED PROGRAMMING DETAIL

(program controller as shown below)

- From Main Menu select **3 - PHASE DATA**
- From PHASE DATA Submenu select **3 - PEDESTRIAN DATA**
- From PEDESTRIAN DATA Submenu select **3 - PED OFFSET+**

```
PHASE.....1...2...3...4...5...6...7...8
WOFF/10  0  50  0  40  0  50  0  40
MODE      0  0  0  0  0  0  0  0
```

CODES: * 0-ADVANCE 1-DELAY

Advance Walk PED programming complete.

INIT & N.A. RESP PROGRAMMING DETAIL

- From Main Menu select **3 - PHASE DATA**
- From PHASE DATA Submenu select **4 - INIT & N.A RESP**

Note Phases 1,3,5 and 7 NOT used!

```
PHASE.....1...2...3...4...5...6...7...8...
INITIAL  0  6  0  1  0  6  0  1
NA RESP  0  1  0  2  0  1  0  2

CODES.....0....1....2....3....4....5....6
INITL  NONE  INACT  RED  YEL  GRN  DRK  G/DW
NA RSP  NONE  NA1  NA2  1&2  ---  ---  ---
***
```

INIT & N.A. RESP PROGRAMMING COMPLETE

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

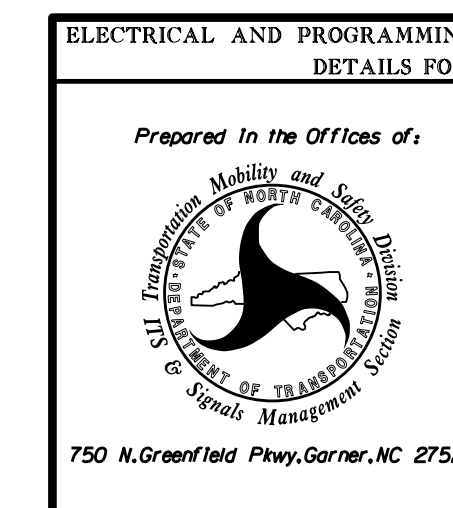
ACCESSIBLE PEDESTRIAN SIGNAL (APS) INSTALLATION NOTES

- Install push buttons and APS equipment per manufacturer's instructions.
- Provide a dedicated cable to each push button per manufacturer's instructions.
- If APS equipment is mounted in cabinet, use filtered power (i.e., Controller Receptacle) to power APS equipment. Do not use Equipment Receptacle, which is a GFCI outlet.
- Never attempt to operate a standard contact closure push button with the APS system unless cabinet is re-wired for standard button operation or unless explicitly allowed by the manufacturer.
- Place manufacturer's instructions in cabinet with cabinet prints, signal plans, and electrical details.
- An APS push button station that is designed to work without the need for interfacing with a pedestrian signal head shall be installed for applications where a push button is installed in a median without a pedestrian signal head.
- A push button with a single tactile arrow that points in both directions of travel shall be installed if the median separates two parallel crosswalks.

This plan supersedes the plan signed and sealed on 5/24/2021.

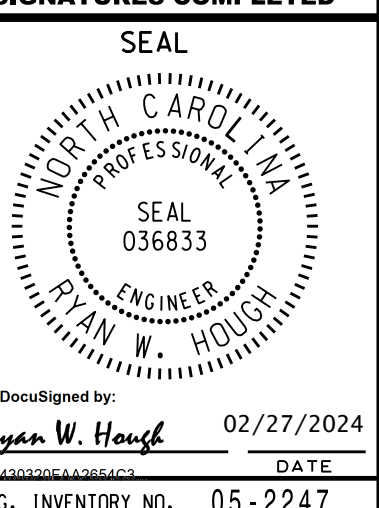
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-2247
DESIGNED: February 2024
SEALED: 02/27/2024
REVISED: N/A

Electrical Detail - Sheet 2 of 2



SR 2215 (Buffaloe Road) at Valley Stream Drive	
Division 5	Wake County
PLAN DATE: February 2024	REVIEWED BY:
PREPARED BY: S.Kirkpatrick	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

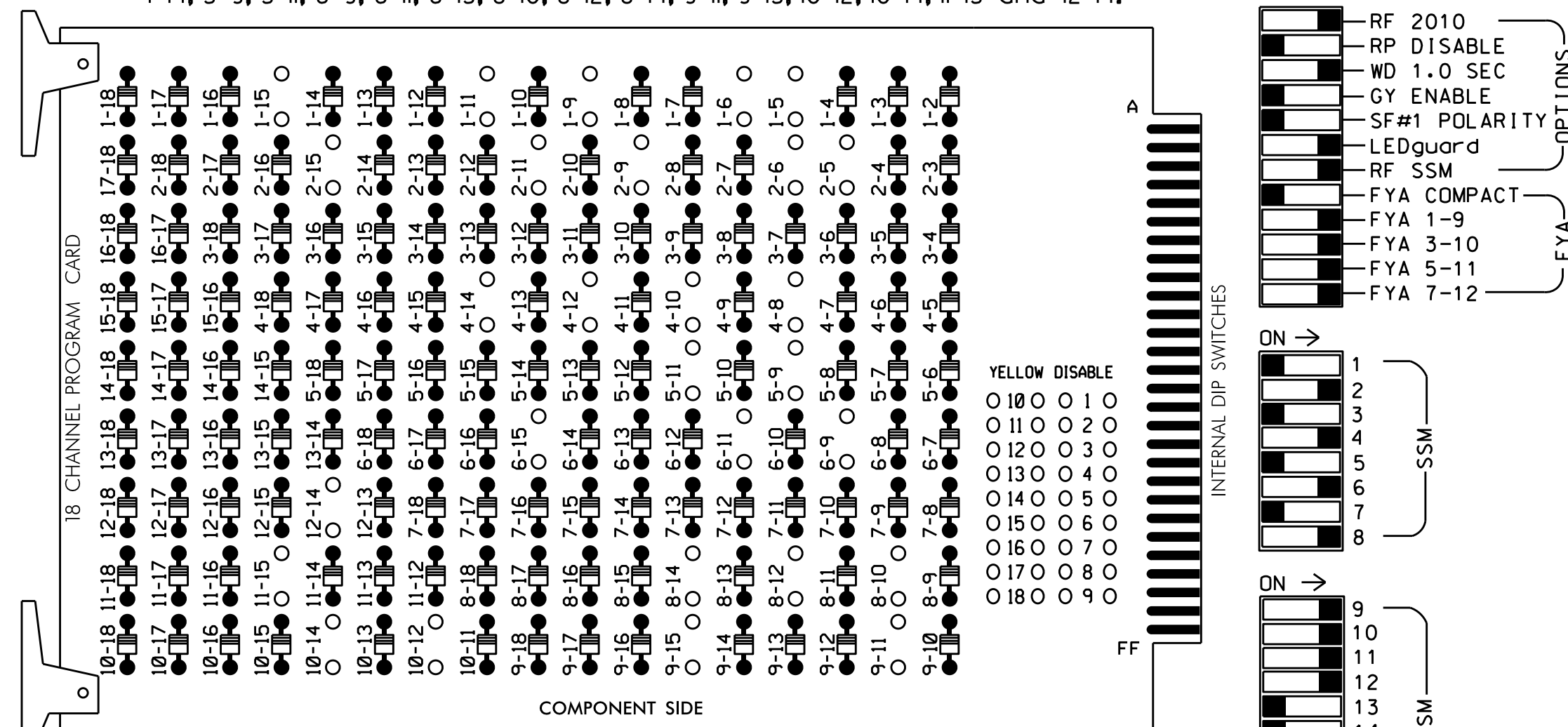


DocuSigned by:
Ryan W. Hough 02/27/2024
DATE
SIG. INVENTORY NO. 05-2247

18 CHANNEL IP CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-15, 4-8, 4-10, 4-12, 4-14, 5-9, 5-11, 6-9, 6-11, 6-15, 8-10, 8-12, 8-14, 9-11, 9-15, 10-12, 10-14, 11-15 and 12-14.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in phases 2 and 6 Green/Don't Walk.
- Enable simultaneous gap-out feature for all phases.
- Program phases 2 and 6 for volume density operation.
- Program phases 4 and 8 for dual entry.
- The cabinet and controller are part of the Raleigh Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070LX
 CABINET.....332 W/ AUX
 SOFTWARE.....SE-PAC2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S6,S7,S8,S9,S11,AUX S1,
 AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,4,4PED,5,6,6PED,8
 OVERLAP "A".....*
 OVERLAP "B".....*
 OVERLAP "C".....*
 OVERLAP "D".....*

* See sheet 2 for Overlap Programming Detail

SIGNAL HEAD HOOK-UP CHART

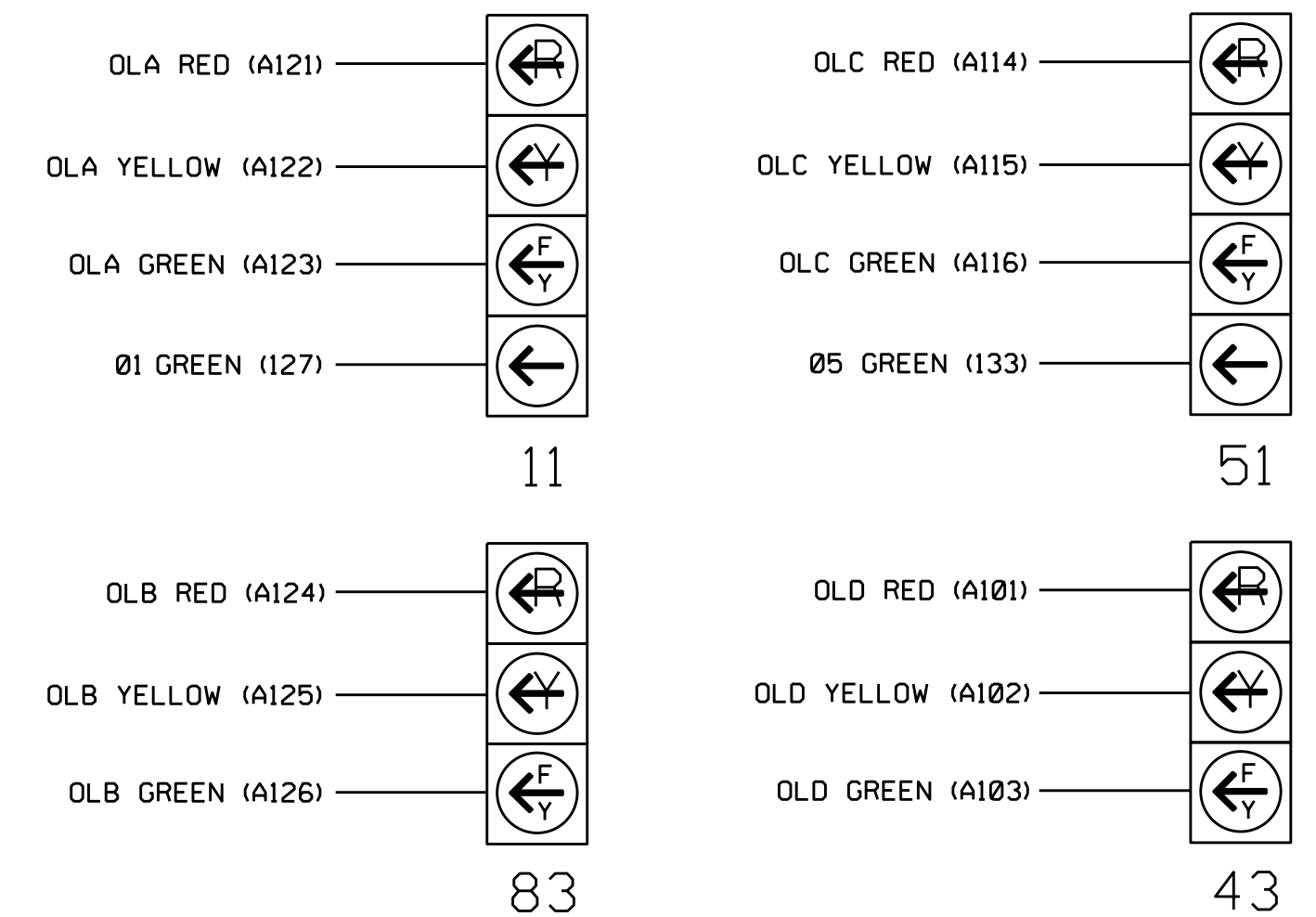
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	P41, P42	51	61,62	P61, P62	NU	81,82	NU	11	83	NU	51	43	NU	
RED	128				101			134			107								
YELLOW	*	129			102		*	135			108								
GREEN		130			103			136			109								
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW																A122	A125	A115	A102
FLASHING YELLOW ARROW																A123	A126	A116	A103
GREEN ARROW	127							133											
Hand icon							104			119									
Person icon							106			121									

NU = Not Used

* See pictorial of head wiring in detail this sheet.
 * Denotes install load resistor. See load resistor installation detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1	Ø 2	-Ø 3	-Ø 4	-Ø 5	Ø 4	-Ø 6	-Ø 7	-Ø 8	-Ø 9	-Ø 10	-Ø 11	-Ø 12	-Ø 13
L	NOT USED	NOT USED	-Ø 3	-Ø 4	-Ø 5	Ø 4	-Ø 6	-Ø 7	-Ø 8	-Ø 9	-Ø 10	-Ø 11	-Ø 12	-Ø 13
U	Ø 5	Ø 6	-Ø 3	-Ø 4	-Ø 5	Ø 8	-Ø 6	-Ø 7	-Ø 8	-Ø 9	-Ø 10	-Ø 11	-Ø 12	-Ø 13
L	NOT USED	Ø 6	-Ø 3	-Ø 4	-Ø 5	Ø 8	-Ø 6	-Ø 7	-Ø 8	-Ø 9	-Ø 10	-Ø 11	-Ø 12	-Ø 13

EX.: 1A, 2A, ETC. = LOOP NO.'S

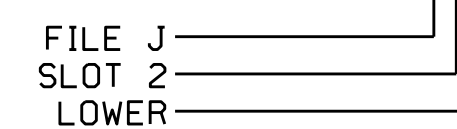
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	DELAY TIME	EXTEND (STRETCH) TIME
1A	TB2-1,2	I1U	56	1	1	5	
2A	TB2-5,6	I2U	39	3	2		
4A	TB4-9,10	I6U	41	11	4	3	
4B	TB4-11,12	I6L	45	12	4	10	
5A	TB3-1,2	J1U	55	19	5	5	
6A	TB3-5,6	J2U	40	21	6		
6B	TB3-7,8	J2L	44	22	6		
8A	TB5-9,10	J6U	42	31	8	3	
8B	TB5-11,12	J6L	46	32	8	10	
PED PUSH BUTTONS							
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED		
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED		

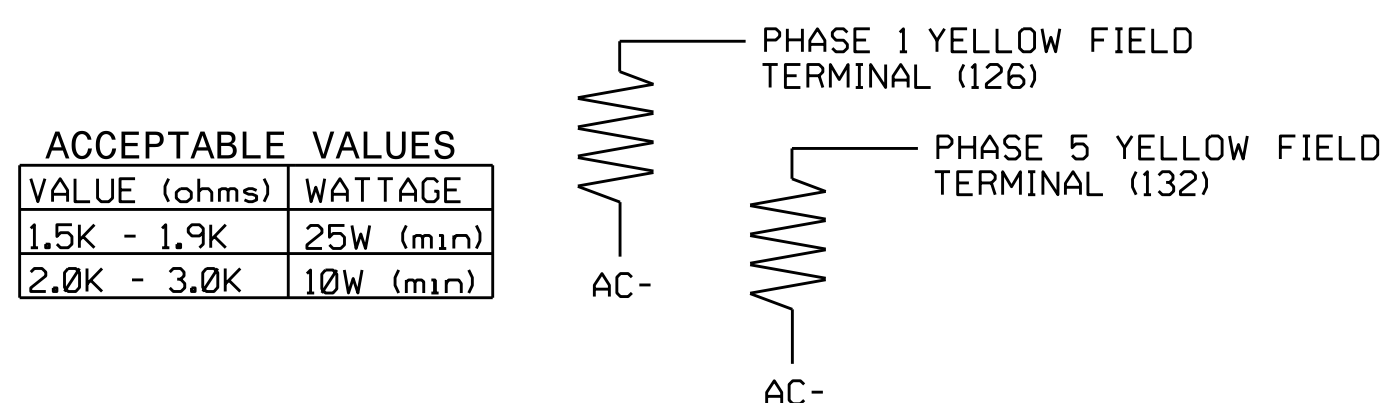
NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

This plan supersedes the plan signed and sealed on 6/1/2021.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1780
 DESIGNED: February 2024
 SEALED: 02/27/2024
 REVISED: N/A

Electrical Detail - Sheet 1 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: Signal Management Solutions, Inc. 750 N. Greenfield Pkwy, Corner, NC 27529	SR 2215 (Buffaloe Road) at SR 2214 (Southall Road)/ Brintons Cottage Street	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 036833 RYAN W. HOUGH
	Division 5 Wake County Raleigh PLAN DATE: February 2024 REVIEWED BY: PREPARED BY: S.Kirkpatrick REVIEWED BY:	

PROGRAMMING DETAILS TO CALL ALTERNATE PHASING

To run the Alternate phasing, schedule a Day Plan that calls an Action that is programmed to enable Phase Function 1.

Actions can be programmed to run free run or call a coordination pattern.

PHASE FUNCTION MAPPING PROGRAMMING DETAIL

Step 1 - Assign OMIT OVERLAPS A & C to Phase Function 1.

1. From Main Menu select 6 - TIME BASE DATA
2. From TIME BASE DATA Submenu select 9 - PHS FUNC MAPPING

Use Up/Dn Keys to position cursor on NUM 1

```

TIME BASE PHS FUNC MAPING
                PHS FUNC SEL(0-OFF/1-ON)
NUM..P-FUNCT NAME.....123456789 0123456
1 PHS-01 MAX # 2    00000000 0000000
2 PHS-02 MAX # 2    00000000 0000000
3 PHS-03 MAX # 2    00000000 0000000
4 PHS-04 MAX # 2    00000000 0000000
UP/DOWN TO SCROLL          E-EDIT
    
```

BEFORE PROCEEDING,
SCROLL THRU ENTIRE
RANGE OF FUNCTIONS TO
ENSURE ALL P-FUNCT 1
NUM x VALUES ARE SET
TO '0' (OFF)

Use Up/Dn/Left/Right keys to position cursor on NUM 145 and program P-FUNCT 1 as shown.

```

TIME BASE PHS FUNC MAPING
                PHS FUNC SEL(0-OFF/1-ON)
NUM..P-FUNCT NAME.....123456789 0123456
145 OVERLAP A OMIT    10000000 0000000
146 OVERLAP B OMIT    00000000 0000000
147 OVERLAP C OMIT    10000000 0000000
148 OVERLAP D OMIT    00000000 0000000
UP/DOWN TO SCROLL          E-EDIT
    
```

SET P-FUNCT 1 VALUES
TO '1' (ON) AS SHOWN
← FOR OVERLAP A OMIT
← FOR OVERLAP C OMIT

PHASE FUNCTION PROGRAMMING COMPLETE

TIME BASE ACTIONS PROGRAMMING

Step 2 - Set up Action numbers to run Phase Function 1.

1. From Main Menu select 6 - TIME BASE DATA
2. From TIME BASE DATA Submenu select 5 - ACTIONS

```

TIME BASE ACTION # ***
                12345678 90123456
PATN:001      PHS: 10000000 00000000
0=I'CONN      AUX: 000-----
1-253=PATN    SPC: 0000000-    0=NO
254=FREE      DIM: 0-----    1=YES
255=FLASH     DET: 000-----
UP/DOWN TO SCROLL
    
```

← NOTICE
PHS 1

SPECIAL FUNCTION PROGRAMMING COMPLETE

*** Action #(s) are to be determined by the Division and/or City Traffic Engineer and are scheduled to run in Day Plan(s).

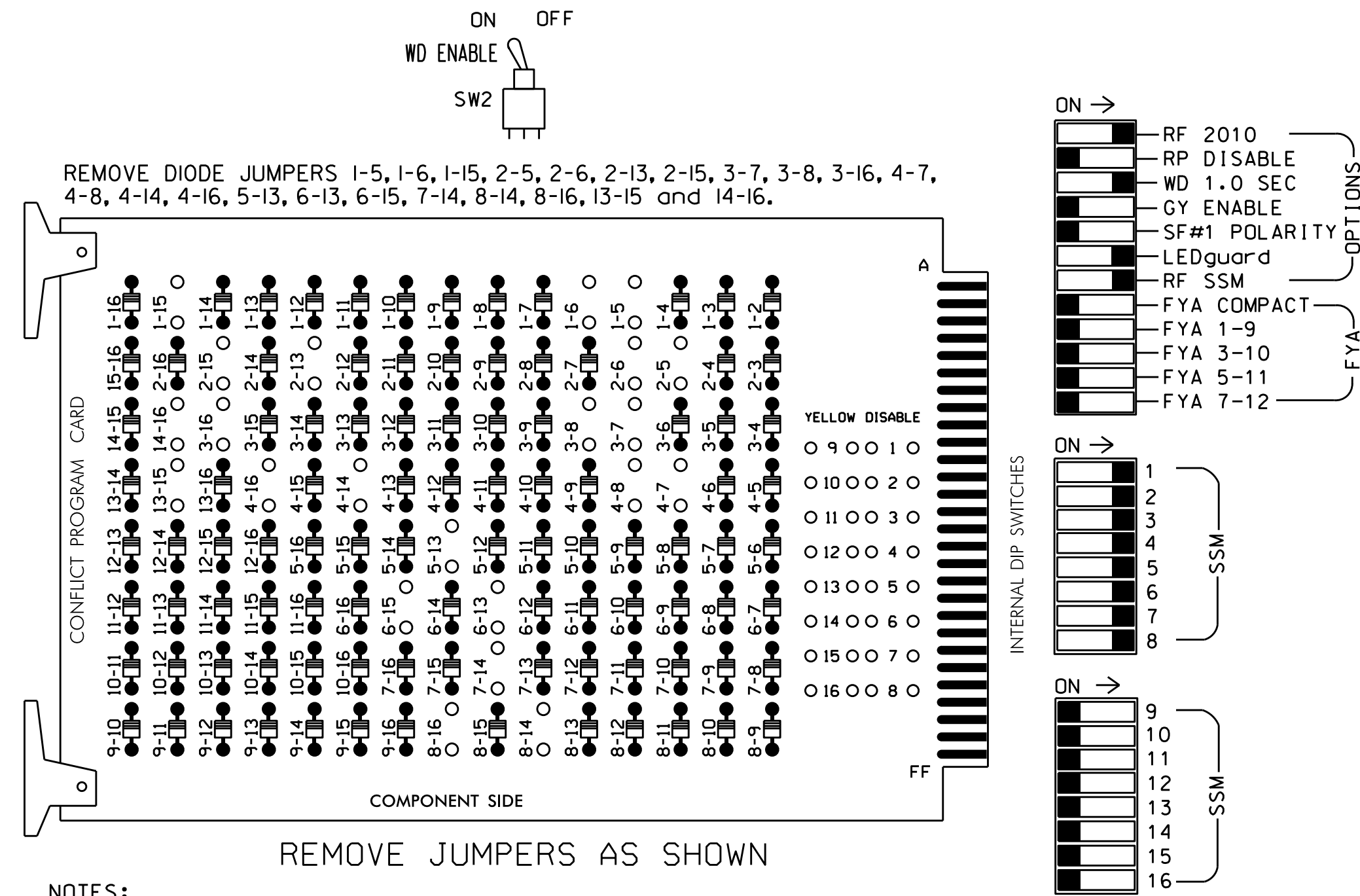
This plan supersedes the plan signed and sealed on 6/1/2021.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1780
DESIGNED: February 2024
SEALED: 02/27/2024
REVISED: N/A

Electrical Detail - Sheet 3 of 3		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED									
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: 750 N. Greenfield Pkwy, Corner, NC 27529	SR 2215 (Buffaloe Road) at SR 2214 (Southall Road)/ Brintons Cottage Street Division 5 Wake County Raleigh PLAN DATE: February 2024 REVIEWED BY: PREPARED BY: S. Kirkpatrick REVIEWED BY:	SEAL SEAL 036833 RYAN W. HOUGH ENGINEER DocuSigned by: Ryan W. Hough 02/27/2024 430020FA2826463 DATE SIG. INVENTORY NO. 05-1780									
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REVISIONS	INIT.	DATE									

16 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 9,10, 11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program controller to start up in phases 2 and 6 green.
- Enable simultaneous gap-out feature for all phases.
- Program phases 2 and 6 for volume density operation.
- The cabinet and controller are part of the Raleigh Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	11,12	21,22 23	P21, P22	31	41,42	P41, P42	51,52	61,62 63	P61, P62	71,72	81,82	P81, P82
RED		128			101			134				107
YELLOW		129			102			135				108
GREEN		130			103			136				109
RED ARROW	125			116				131			122	
YELLOW ARROW	126			117				132			123	
GREEN ARROW	127			118				133			124	
			113			104		119				110
			115			106		121				112

NU = Not Used

EQUIPMENT INFORMATION

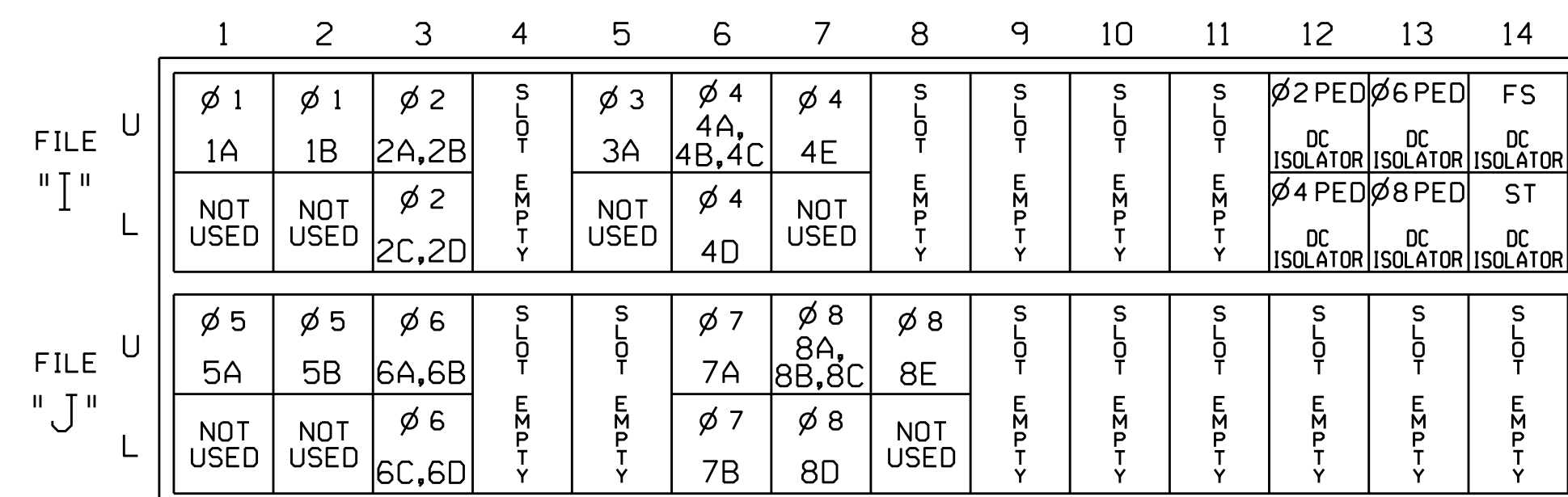
CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....SE-PAC2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S4P,S5,S6,S6P,S7,S8,S8P
 PHASES USED.....1,2,3,4,5,6,7,8,2 PED,4 PED,6 PED,8 PED
 OVERLAPS.....NONE

ACCESSIBLE PEDESTRIAN SIGNAL (APS) INSTALLATION NOTES

- Install push buttons and APS equipment per manufacturer's instructions.
- Provide a dedicated cable to each push button per manufacturer's instructions.
- If APS equipment is mounted in cabinet, use filtered power (i.e., Controller Receptacle) to power APS equipment. Do not use Equipment Receptacle, which is a GFCI outlet.
- Never attempt to operate a standard contact closure push button with the APS system unless cabinet is re-wired for standard button operation or unless explicitly allowed by the manufacturer.
- Place manufacturer's instructions in cabinet with cabinet prints, signal plans, and electrical details.
- An APS push button station that is designed to work without the need for interfacing with a pedestrian signal head shall be installed for applications where a push button is installed in a median without a pedestrian signal head.
- A push button with a single tactile arrow that points in both directions of travel shall be installed if the median separates two parallel crosswalks.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

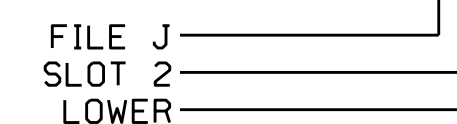
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	DELAY TIME	EXTEND (STRETCH) TIME
1A	TB2-1,2	I1U	56	1	1		
1B	TB2-5,6	I2U	39	3	1		
2A,2B	TB2-9,10	I3U	63	5	2		
2C,2D	TB2-11,12	I3L	76	6	2		
3A	TB4-5,6	I5U	58	9	3		
4A,4B,4C	TB4-9,10	I6U	41	11	4		3.1
4D	TB4-11,12	I6L	45	12	4		
4E	TB6-1,2	I7U	65	13	4		
5A	TB3-1,2	J1U	55	19	5		
5B	TB3-5,6	J2U	40	21	5		
6A,6B	TB3-9,10	J3U	64	23	6		
6C,6D	TB3-11,12	J3L	77	24	6		
7A	TB5-9,10	J6U	42	31	7	3	
7B	TB5-11,12	J6L	46	32	7		
8A,8B,8C	TB7-1,2	J7U	66	33	8		3.1
8D	TB7-3,4	J7L	79	34	8		
8E	TB7-5,6	J8U	50	35	8		
PED PUSH BUTTONS							
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED		
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED		
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED		
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED		

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

INPUT FILE POSITION LEGEND: J2L



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

This Electrical Detail supersedes the detail sealed on 10-13-22

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0139
 DESIGNED: February 2024
 SEALED: 02-27-24
 REVISED: N/A

Electrical Detail

Electrical and Programming Details For:

US 401 (Louisburg Rd.) at SR 2036/2108 (N. New Hope Road)

Division 5 Wake County Raleigh

PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS	INIT.	DATE

750 N. Greenfield Pkwy, Corner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

NORTH CAROLINA PROFESSIONAL ENGINEER

RYAN W. HOUGH

02/29/2024

SIG. INVENTORY NO. 05-0139

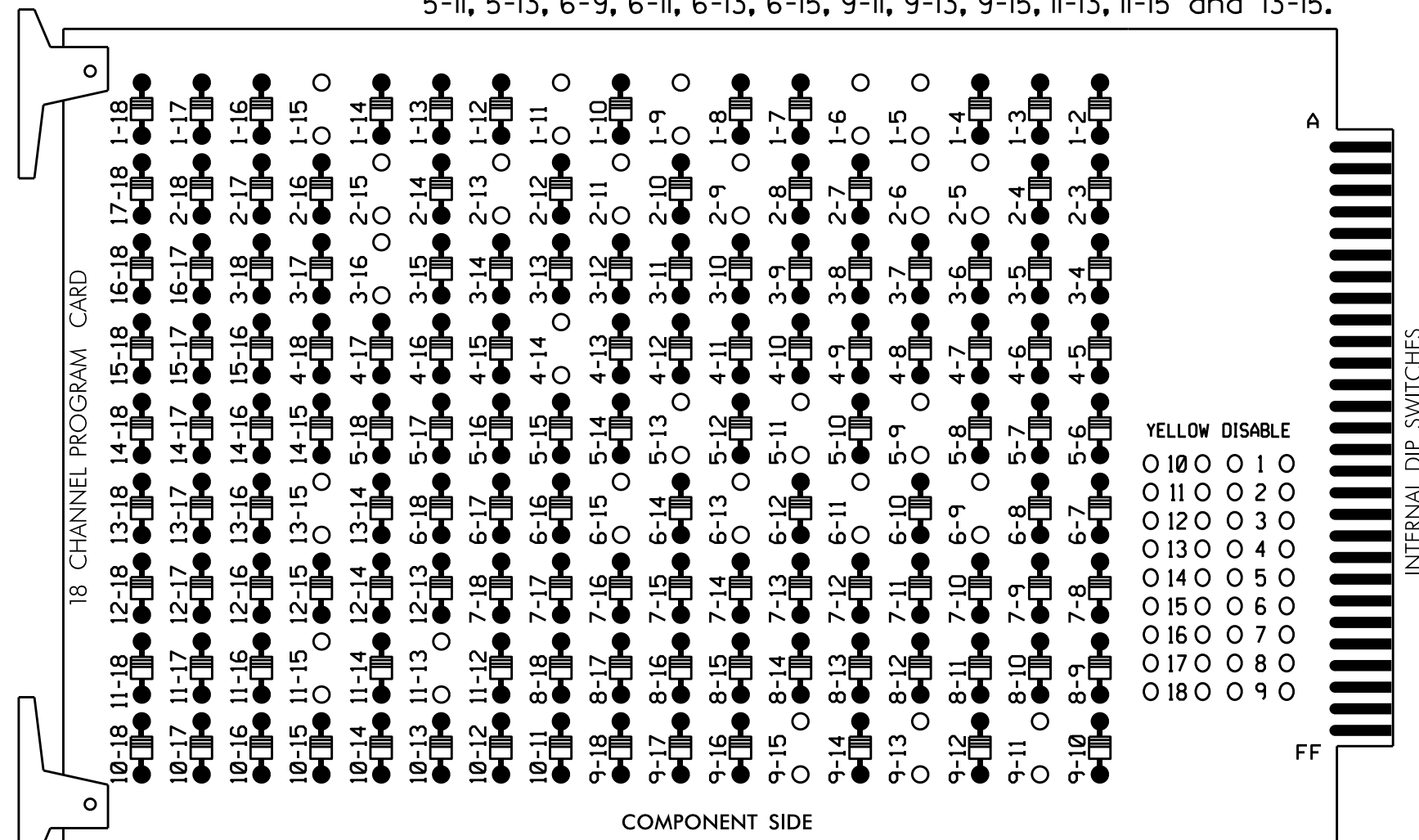
28-FEB-2024 07:28
 S:\17565\17565\17565\SIGNAL\WORKGROUPS\519_MonPeterson\050139_sm.dwg 20221013.dgn
 JTPeterson

18 CHANNEL CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

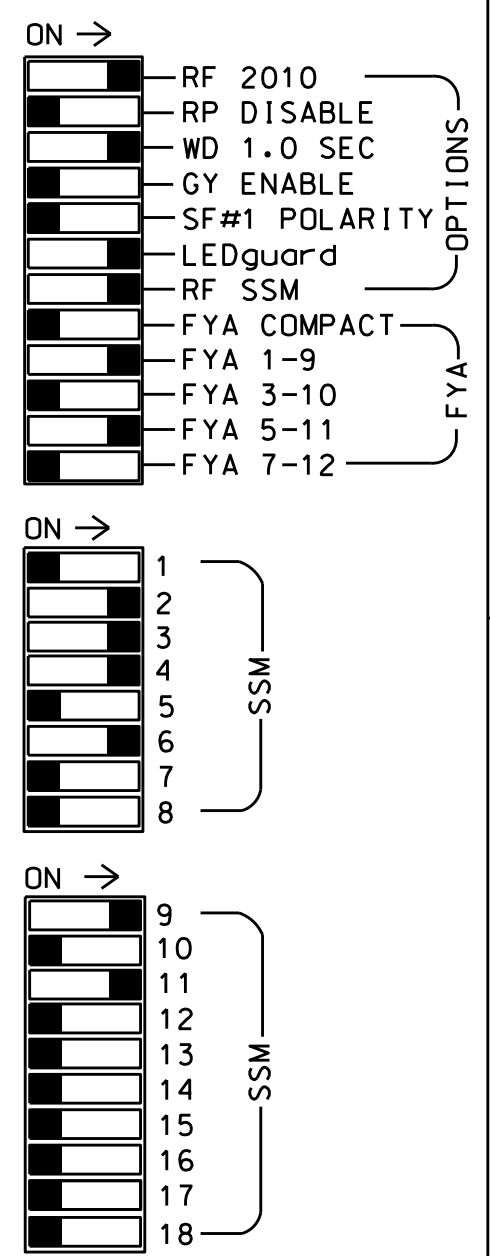
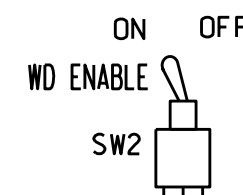
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-16, 4-14, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 9-11, 9-13, 9-15, 11-13, 11-15 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in phases 2 and 6 green.
- Enable simultaneous gap-out feature for all phases.
- The cabinet and controller are part of the Raleigh Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....SE-PAC2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S8,S12,
 AUX S1,AUX S2,AUX S3
 PHASES USED.....1,2,2PED,3,3PED,4,4PED,6,6PED
 OVERLAP A.....*
 OVERLAP B.....*
 OVERLAP C.....NOT USED
 OVERLAP D.....NOT USED
 OVERLAP E.....*
 OVERLAP F.....NOT USED
 OVERLAP G.....1+4

* SEE SHEET 2 FOR OVERLAP PROGRAMMING

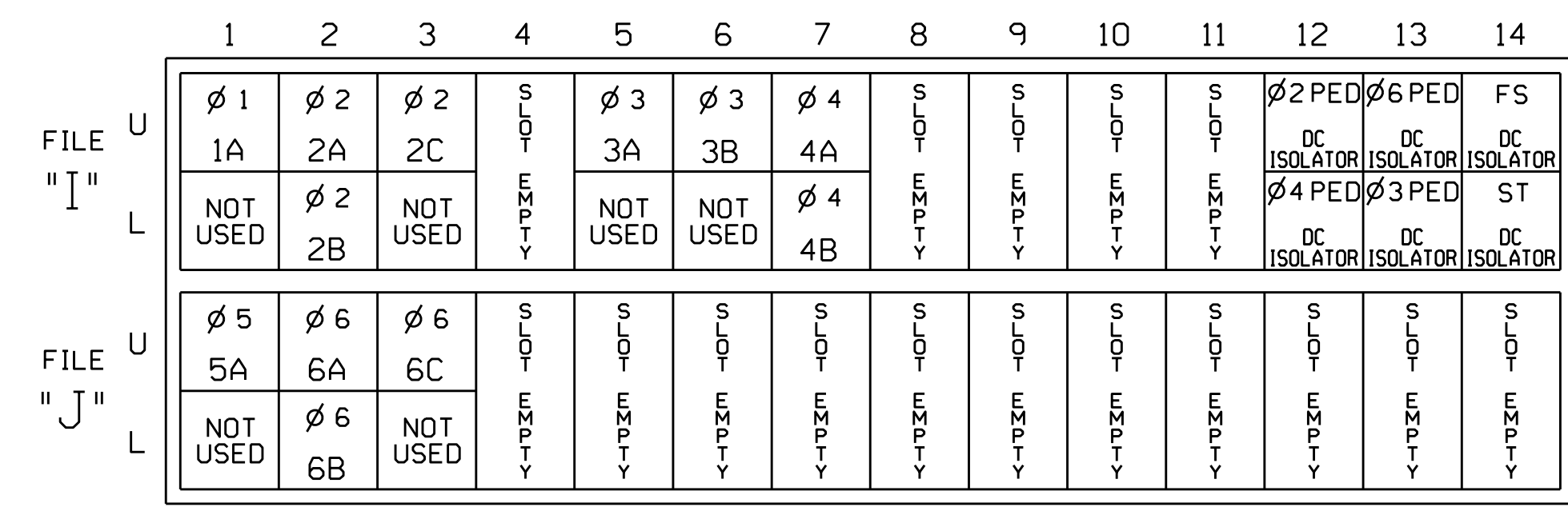
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OLG	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	OLE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	33	41,42	P41, P42	NU	61,62	NU	NU	NU	P31, P32	11	33	31,32	NU	NU	NU
RED		128			101			134							A124			
YELLOW	*	129		*	102			135										
GREEN		130			103			136										
RED ARROW															A121		A111	
YELLOW ARROW															A122	A125	A112	
FLASHING YELLOW ARROW															A123	A126		
GREEN ARROW	127				118													A113
Hand					113			104				110						
Person					115			106				112						

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ** See Phase 3 PED output programming detail on sheet 2.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

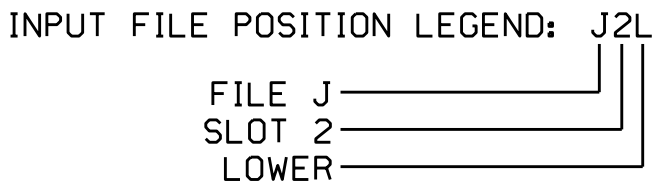
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

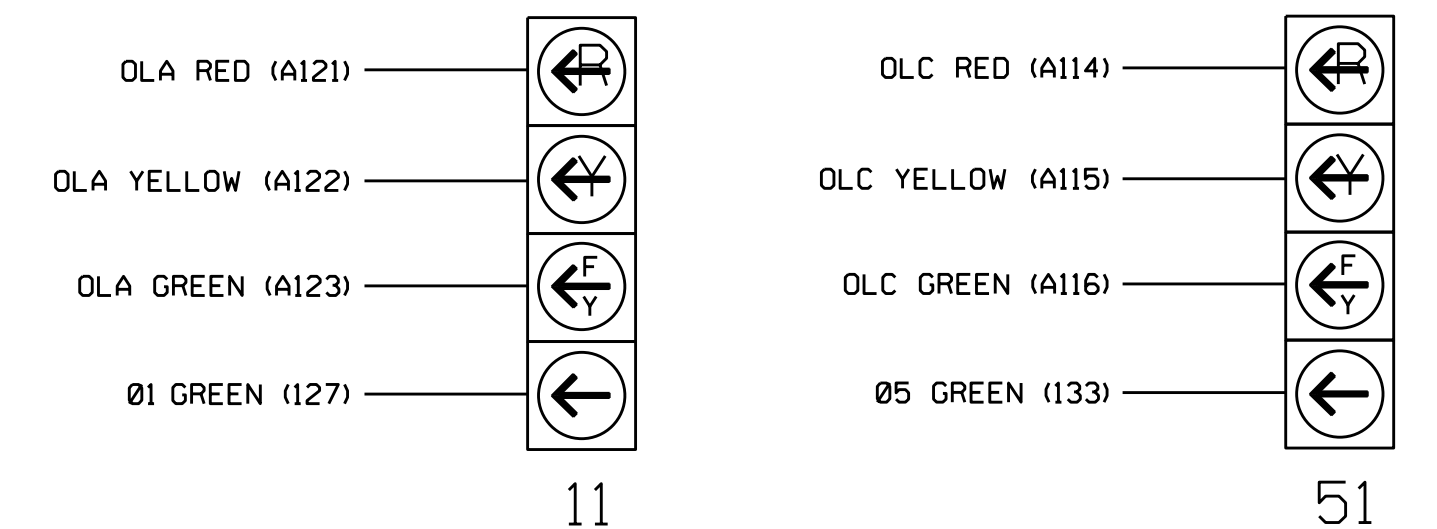
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	DELAY TIME	EXTEND (STRETCH) TIME
1A	TB2-1,2	11U	56	1	1	5	
2A	TB2-5,6	12U	39	3	2		
2B	TB2-7,8	12L	43	4	2		
1B	TB2-9,10	13U	63	5	2		
3A	TB4-5,6	15U	58	9	3	3	
3B	TB4-9,10	16U	41	11	3	10	
6A	TB3-5,6	J2U	40	21	6		
6B	TB3-7,8	J2L	44	22	6		
PED PUSH BUTTONS							
P21,P22	TB8-4,6	112U	67	PED 2	2 PED		
P41,P42	TB8-5,6	112L	69	PED 4	4 PED		
P31,P32	TB8-8,9	113L	70	PED 8	3 PED		

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

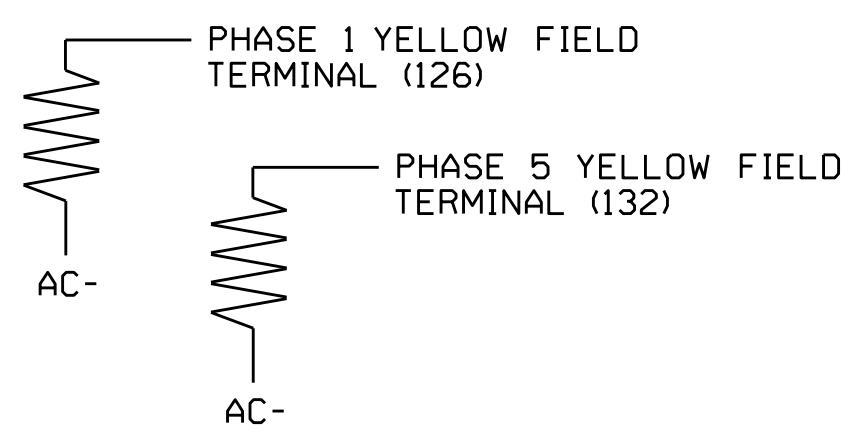


NOTE: See sheet 2 for Protected & Permitted Phases programming.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3

Electrical and Programming Details For: US 401 (Louisburg Road) at Calvary Drive and State Employee's Credit Union

Division 5 Wake County Raleigh

PLAN DATE: February 2024 REVIEWED BY:

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS: INIT. DATE

DocuSigned by: Ryan W. Haug 02/29/2024

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL: NORTH CAROLINA PROFESSIONAL ENGINEER RYAN W. HAUG 036833

SIG. INVENTORY NO. 05-1902

28-FEB-2024 07:24 S:\17565\K15\Sig\01\Wor\Haug\051902_sm.dwg JTP:erison

PROGRAMMING DETAILS TO CALL ALTERNATE PHASING

To run the Alternate phasing, schedule a Day Plan that calls an Action that is programmed to enable Phase Function 1.

Actions can be programmed to run free run or call a coordination pattern.

PHASE FUNCTION MAPPING PROGRAMMING DETAIL

Step 1 - Assign OMIT OVERLAPS A & C to Phase Function 1.

- From Main Menu select 6 - TIME BASE DATA
- From TIME BASE DATA Submenu select 9 - PHS FUNC MAPPING

Use Up/Dn Keys to position cursor on NUM 1

```

TIME BASE PHS FUNC MAPING
                PHS FUNC SEL(0-OFF/1-ON)
NUM..P-FUNCT NAME.....123456789 0123456
1 PHS-01 MAX # 2 00000000 0000000
2 PHS-02 MAX # 2 00000000 0000000
3 PHS-03 MAX # 2 00000000 0000000
4 PHS-04 MAX # 2 00000000 0000000
UP/DOWN TO SCROLL          E-EDIT
    
```

BEFORE PROCEEDING,
SCROLL THRU ENTIRE
RANGE OF FUNCTIONS TO
ENSURE ALL P-FUNCT 1
NUM x VALUES ARE SET
TO '0' (OFF)

Use Up/Dn/Left/Right keys to position cursor on NUM 145 and program P-FUNCT 1 as shown.

```

TIME BASE PHS FUNC MAPING
                PHS FUNC SEL(0-OFF/1-ON)
NUM..P-FUNCT NAME.....123456789 0123456
145 OVERLAP A OMIT 10000000 0000000
146 OVERLAP B OMIT 00000000 0000000
147 OVERLAP C OMIT 10000000 0000000
148 OVERLAP D OMIT 00000000 0000000
UP/DOWN TO SCROLL          E-EDIT
    
```

SET P-FUNCT 1 VALUE
TO '1' (ON) AS SHOWN
← FOR OVERLAP A OMIT
← FOR OVERLAP C OMIT

PHASE FUNCTION PROGRAMMING COMPLETE

TIME BASE ACTIONS PROGRAMMING

Step 2 - Set up an Action to run Phase Function 1.

- From Main Menu select 6 - TIME BASE DATA
- From TIME BASE DATA Submenu select 5 - ACTIONS

```

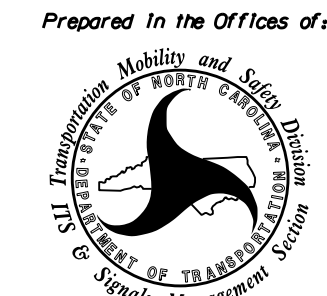
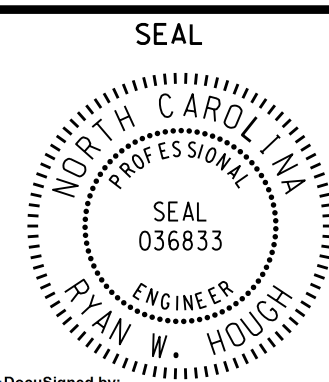
TIME BASE ACTION # ***
                12345678 90123456
PATN:001      PHS: 10000000 00000000 ← NOTICE
0=I'CONN      AUX: 000----- PHS 1
1-253=PATN    SPC: 0000000-   0=NO
254=FREE      DIM: 0-----   1=YES
255=FLASH     DET: 000-----
UP/DOWN TO SCROLL
    
```

SPECIAL FUNCTION PROGRAMMING COMPLETE

*** Action #(s) are to be determined by the Division and/or City Traffic Engineer and are scheduled to run in Day Plan(s).

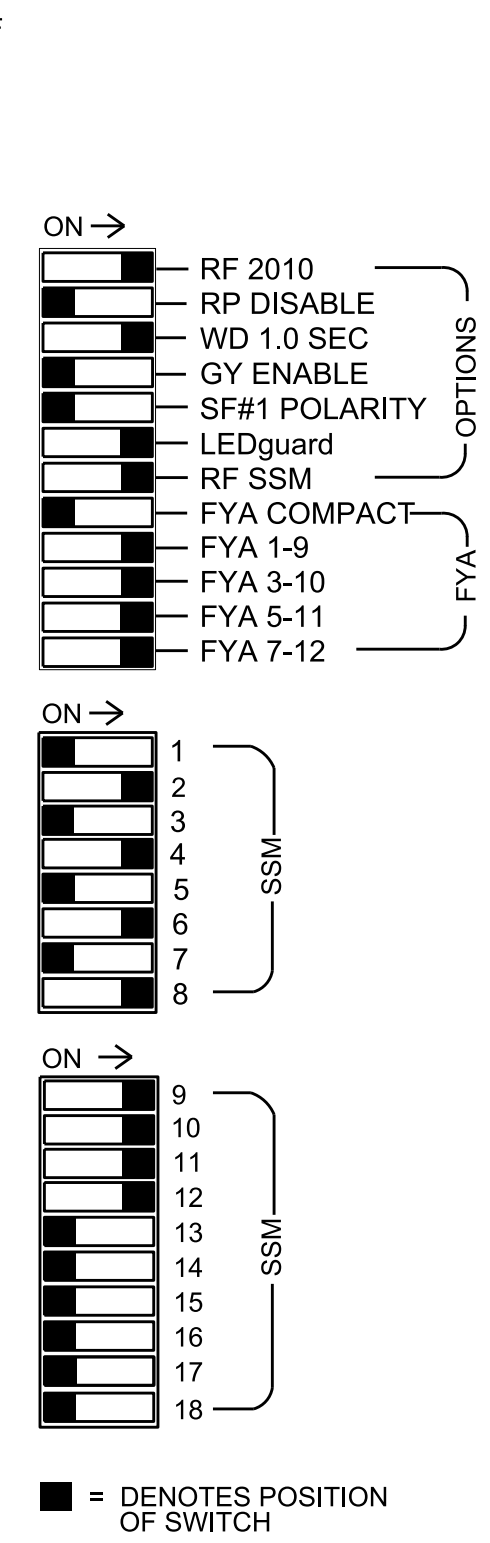
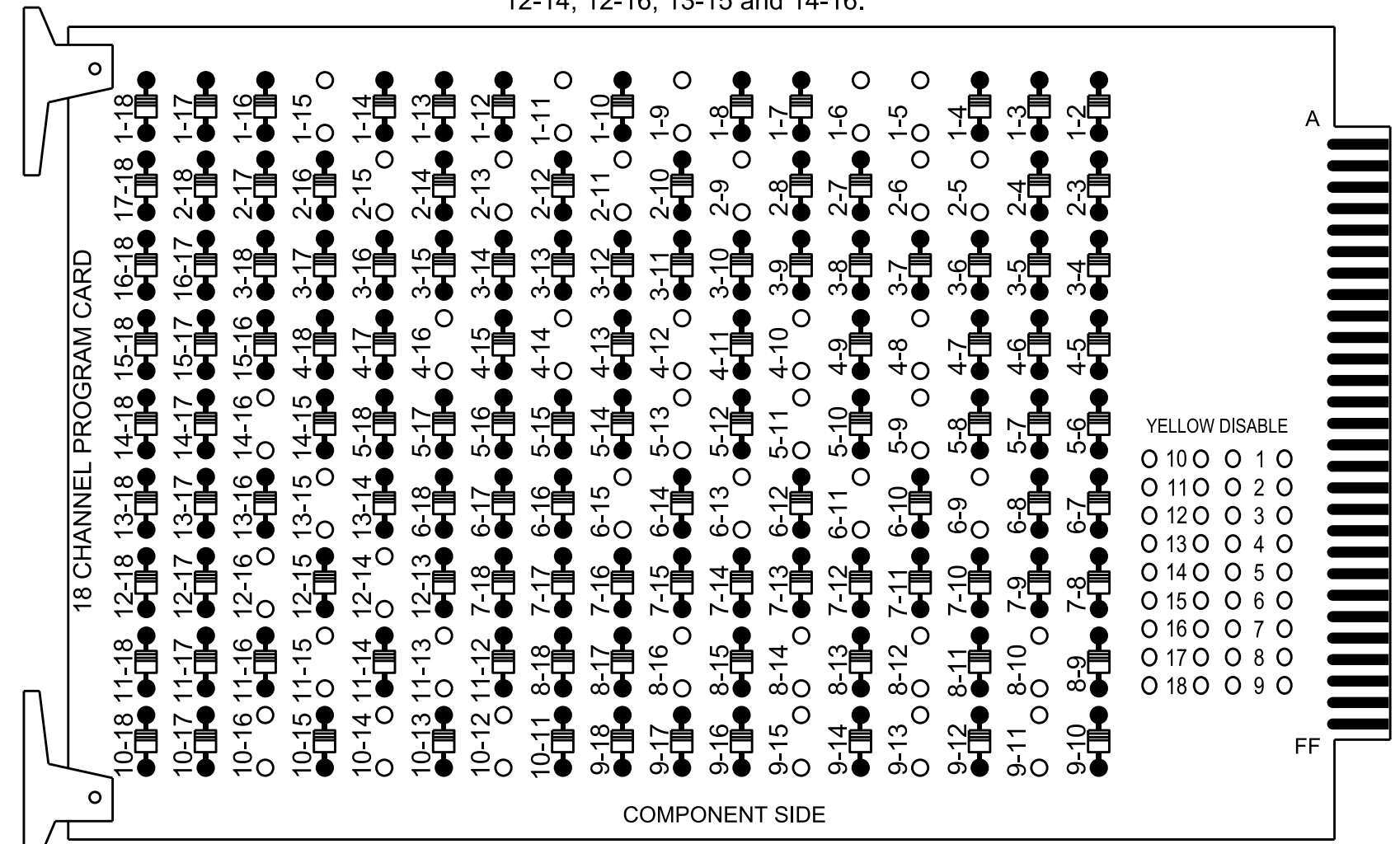
This Electrical Detail supersedes the detail sealed on 10-18-22

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1902
DESIGNED: February 2024
SEALED: 02-27-24
REVISED: N/A

Electrical Detail - Sheet 3 of 3		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED									
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:  750 N. Greenfield Pkwy, Corner, NC 27529	US 401 (Louisburg Road) at Calvary Drive and State Employee's Credit Union Division 5 Wake County Raleigh PLAN DATE: February 2024 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY:	SEAL  DocuSigned by: Ryan W. Hough 02/29/2024 430320FAA285403 DATE SIG. INVENTORY NO. 05-1902									
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">REVISIONS</th> <th style="width: 25%;">INIT.</th> <th style="width: 25%;">DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>		REVISIONS	INIT.	DATE							
REVISIONS	INIT.	DATE									

18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)
 REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that the Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....18 With Aux. Output File
 Load Switches Used.....S1, S2, S3, S5, S6, S7, S8, S9, S11, S12, AUX S1, AUX S2, AUX S4, AUX S5
 Phases Used.....1, 2, 2PED, 4, 4PED, 5, 6, 6PED, 8, 8PED
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....*
 Overlap "4".....*

*See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	NU	42,43	P41, P42	51	61,62	P61, P62	NU	82,83	P81, P82	11	81	NU	51	41	NU
RED		128			101			134		107								
YELLOW	*	129			102		*	135		108								
GREEN		130			103			136		109								
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127							133										
Hand																		
Walking																		

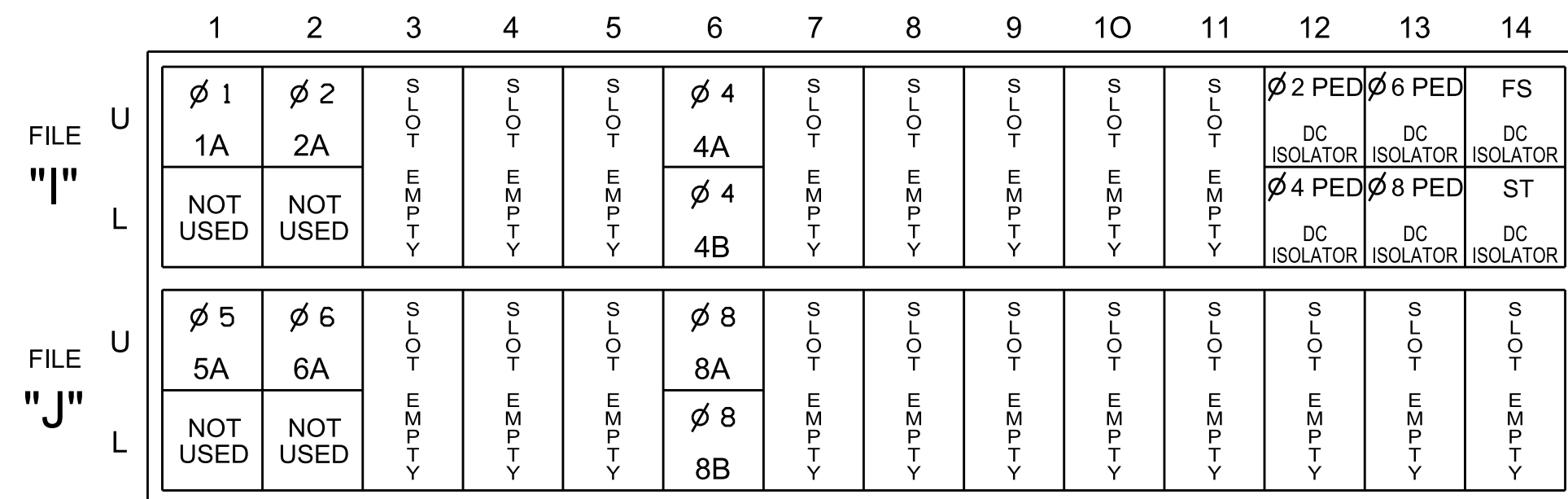
* Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.
 NU = Not Used

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

INPUT FILE POSITION LAYOUT

(front view)

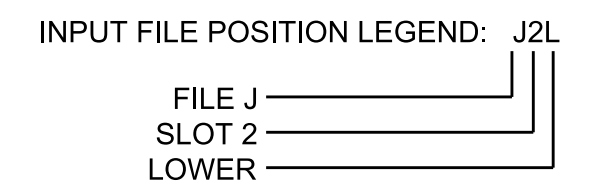


EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

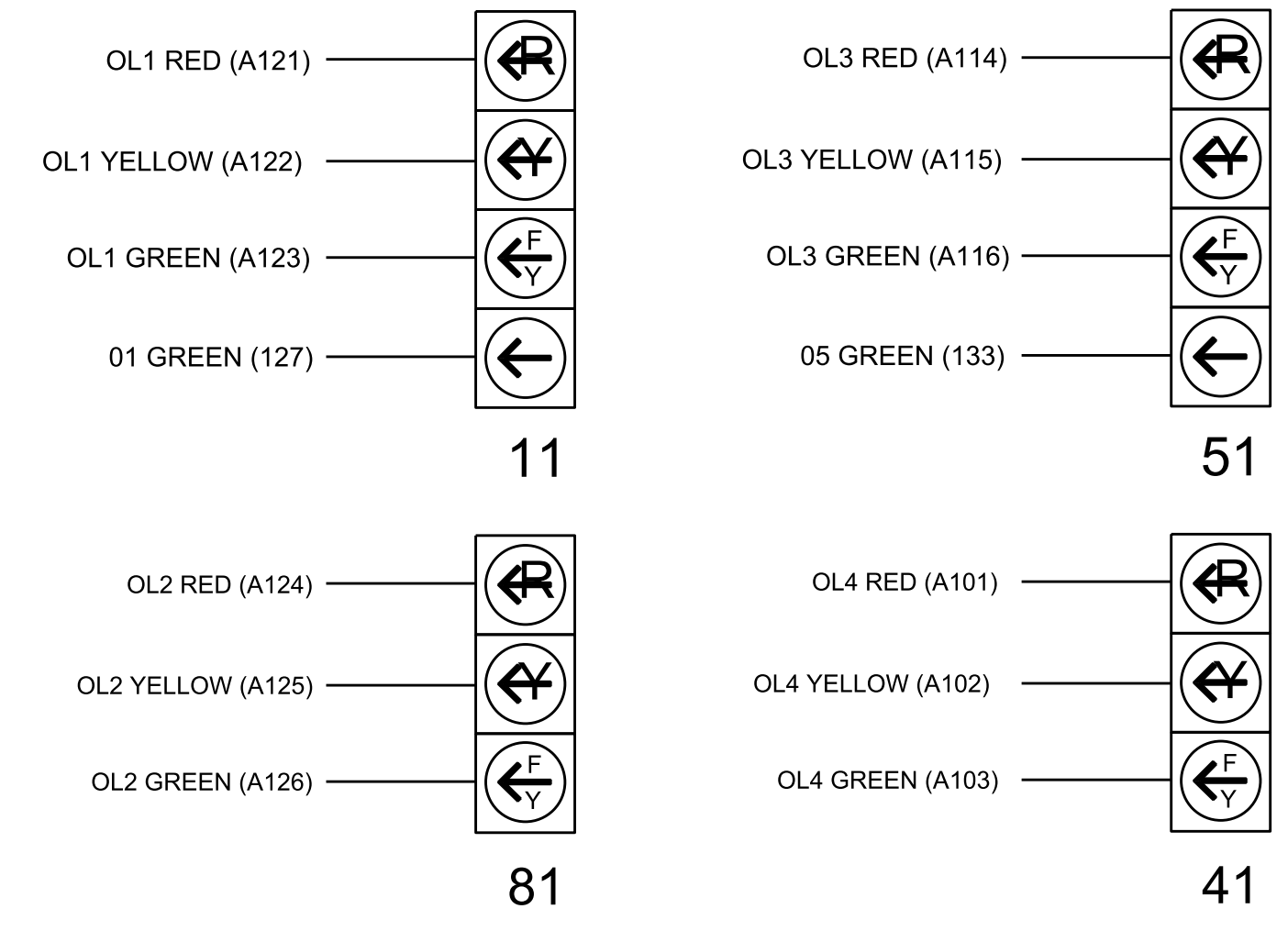
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
1A	TB2-1,2	I1U	56	18	1	1*	15		X		X	
				-	29	6*	3		X	X	X	
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
4A	TB4-9,10	I6U	41	3	8	4	3		X	X	X	
4B	TB4-11,12	I6L	45	7	9	4	10		X	X	X	
5A	TB3-1,2	J1U	55	17	15	5*	15		X	X	X	
				-	31	2*	3		X	X	X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
8A	TB5-9,10	J6U	42	4	22	8	3		X	X	X	
8B	TB5-11,12	J6L	46	8	23	8	10		X	X	X	

*For the detectors to work as shown on the signal plan see the Detector Programming Detail for Alternate Phasing on sheet 2 of this plan.



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

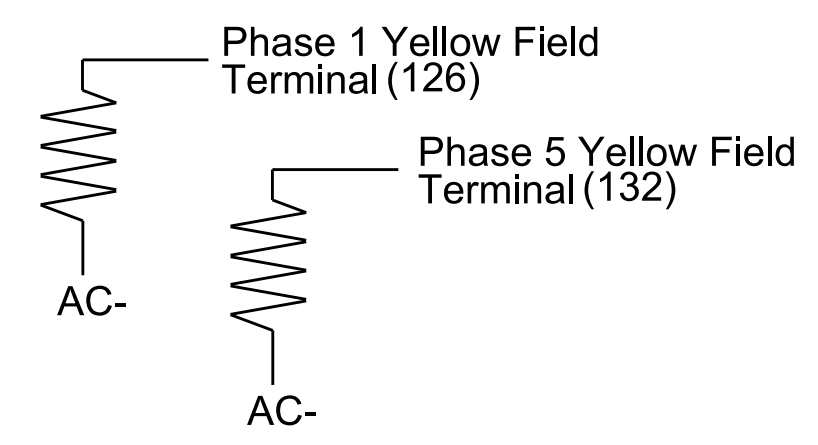
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1319
 DESIGNED: March 2023
 SEALED: 4/13/23
 REVISED:

Electrical Detail - Sheet 1 of 2

Prepared in the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

SR 1142 (Humie Olive Road) at Blazing trail Drive and Apex Friendship Elementary School
 Division 5 Wake County Apex
 PLAN DATE: April 2023 REVIEWED BY: DTJ
 PREPARED BY: D.J. Craddock REVIEWED BY:

REVISIONS: INIT. DATE

DocuSigned by: *Todd Joyce* 04/13/2023
 SEAL 031001
 ENGINEER TODD JOYCE
 SIG. INVENTORY NO. 05-1319

13-APR-2023 11:37 S:\IT\SS\KTS\Sig\051319\1319_4100070243\W051319.sm.ele_202304.dcd.dgn
 13-APR-2023 11:37 S:\IT\SS\KTS\Sig\051319\1319_4100070243\W051319.sm.ele_202304.dcd.dgn
 13-APR-2023 11:37 S:\IT\SS\KTS\Sig\051319\1319_4100070243\W051319.sm.ele_202304.dcd.dgn

MAXTIME ALTERNATE PHASING ACTIVATION DETAIL

To run alternate phasing, select a Pattern that is programmed to run Overlap Plan 2 and Detector Plan 2.
A Pattern can be selected through the scheduler or manually by changing the Operational Mode.

PHASING	OVERLAP PLAN	VEH DET PLAN
ACTIVE PLAN REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PLAN REQUIRED TO RUN ALTERNATE PHASING	2	2

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN OVERLAP 2 AND VEHICLE DETECTOR PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAP PLAN 2: Modifies overlap included phases for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

MAXTIME OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	2	4	6	8
Modifier Phases	1		5	
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

MAXTIME OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

In the table view of the web interface, right click on "Overlap" in the top left corner of the table. Copy the entire contents of Overlap Plan 1. Paste Overlap Plan 1 into Overlap Plan 2. Modify Overlap Plan 2 as shown below and save changes.

Overlap Plan 2

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	-	4	-	8
Modifier Phases	1		5	
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

NOTICE INCLUDED PHASES

MAXTIME ALTERNATE PHASING PATTERN PROGRAMMING DETAIL

Front Panel
Main Menu >Controller >Coordination >Patterns

Web Interface
Home >Controller >Coordination >Patterns

Pattern	Veh Det Plan	Overlap Plan
*	2	2

* The Pattern number(s) are to be determined by the Division and/or City Traffic Engineer.

MAXTIME DETECTOR PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOPS 1A & 5A

Front Panel
Main Menu >Controller >Detector >Veh Det Plans

Web Interface
Home >Controller >Detector Configuration >Vehicle Detectors

In the table view of web interface right click on "Detector" in the top left corner of the table. Copy the entire contents of Detector Plan 1. Paste Detector Plan 1 into Detector Plan 2. Modify Detector Plan 2 as shown below and save changes.

Plan 2

Detector	Call Phase	Delay
1	1	3
29	0	-

1A


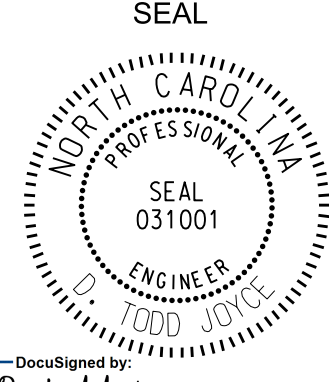
Detector	Call Phase	Delay
15	5	3
31	0	-

5A

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1319
DESIGNED: March 2023
SEALED: 4/13/23
REVISED:

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

 Prepared in the Offices of: Transportation Mobility and Safety Division WAKE COUNTY DEPARTMENT OF TRANSPORTATION & SIGNALS MANAGEMENT SECTION 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1142 (Humie Olive Road) at Blazing trail Drive and Apex Friendship Elementary School	SEAL  SEAL 031001 TODD JOYCE ENGINEER
	Division 5 Wake County Apex PLAN DATE: April 2023 REVIEWED BY: DTJ PREPARED BY: D.J. Craddock REVIEWED BY:	REVISIONS INIT. DATE